U.S. DEPARTMENT OF COMMERCÉ PATENT AND TRADEMARK OFFICE ATTY, DOCKET NO. 851063.438

January 25, 2000

APPLICATION NO. 09/490,589

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANT Leonardo Fragapane FILING DATE

GROUP ART U 2814

IIS PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AE						
	AD		-	9			
	AE						
	AF						
	AG						
	АН						-
	ΑI						
	ΑJ						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY		CLASS	SUBCLASS	TRANS	LATION
					*			YES	NO
HP	AL	JP06140632	20/05/94	Japan (Abstract Only)					
HP	АМ	WO91/17570	14/11/91	PCT			/		
HP	AN	EP0450082A1	09/10/91	Europe					
HP	AO	JP60249367	10/12/85	Japan (Abstract Only)					
	AP		- 32						

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

HP.	AR	Chow, T.P. et al., "P-Channel, Vertical Insulated Gate Bipolar Transistors With Collector Short," <i>International Electron Devices Meeting, Washington</i> , Dec. 6-9, 1987, pp. 670-673.
	AS	

EXAMINER

DATE CONSIDERED

11/29/00

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).